

Electronic Acknowledgement Receipt

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| EFS ID: | 1511509 |
| Application Number: | 10575041 |
| International Application Number: | |
| Confirmation Number: | 5951 |
| Title of Invention: | Processor For Controlling Performance In Accordance With A Chip Temperature, Information Processing Apparatus, And Method Of Controlling Processor |
| First Named Inventor/Applicant Name: | Kenichi Adachi |
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| Filer: | Matthew B. Dernier/Ute Wojtkowski |
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| Attorney Docket Number: | 545/72 |
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| Time Stamp: | 10:54:16 |
| Application Type: | U.S. National Stage under 35 USC 371 |

Payment information:

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| Submitted with Payment | no |
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) | Multi Part /.zip | Pages (if appl.) |
|-----------------|--|------------------------|------------------|------------------|------------------|
| 1 | Information Disclosure Statement (IDS) Filed | 545-72_IDS_2-13-09.pdf | 694082 | no | 4 |

Warnings:

Information:

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Foreign Reference

545-72_IPRP_ref.pdf

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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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